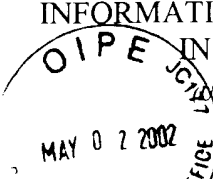


Form PTO-1449 INFORMATION DISCLOSURE CITATION IN AN APPLICATION (Use several sheets if necessary) 	Docket Number: 204552016500	Application Number: 09/412,328
	Applicant Yuichi SATO	
	Filing Date: October 5, 1999	Group Art Unit: 2814
	Mailing Date: May 2, 2002	

U.S. PATENT DOCUMENTS

Examiner Initials	Ref. No.	Date	Document No.	Name	Class	Subclass	Filing Date If Appropriate
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FOREIGN PATENT DOCUMENTS

Examiner Initials	Ref. No.	Date	Document No.	Country	Class	Subclass	Translation YES NO
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OTHER DOCUMENTS

(including author, title, Date, Pertinent Pages, Etc.)

Examiner Initials	Ref. No.	Title
2-1	1.	Assaderaghi et al., 1994 "A dynamic threshold voltage MOSFET (DTMOS) for very low voltage operation" IEEE Electron Device Letters Vol. 15, pp 510-512.
(2.	Andoh et al., 1994 "Design methodology for low-voltage MOSFETS" IEEE International Electron Devices Meeting, Technical Digest, pp 79-82.
	3.	Assaderaghi et al., 1994 "A dynamic threshold voltage MOSFET (DTMOS) for ultra-low voltage operation" IEEE International Electron Devices Meeting, Technical Digest, pp 809-812.

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